

Third International Workshop on  
**FPGAs for Software Programmers (FSP 2016)**

August 29, 2016, Lausanne, Switzerland

co-located with

International Conference on Field Programmable Logic and Applications (FPL)

**Program**

- 09:00 - 09:05 Workshop Opening
- 09:05 - 10:00 **Keynote 1: "A Comprehensive Approach to Software-Defined FPGA Computing"**  
*Gael Paul, PLDA*
- 10:00 - 10:30 **"Automated Generation of Reconfigurable Systems-on-Chip by Interactive Code Transformations for High-Level Synthesis"**  
*Silvano Brugnoli, Thomas Corbat, Peter Sommerlad, Toni Suter, Jens Korinth, David de la Chevallerie, Andreas Koch*
- 10:30 - 11:00 Coffee Break
- 11:00 - 11:30 **"A Compute Model for Generating High Performance Computing SoCs on Hybrid Systems with FPGAs"**  
*Felix Friedrich, Oleksii Morozov, Patrick Hunziker*
- 11:30 - 12:15 **Keynote 2: "An Open Ecosystem for Software Programmers to Compute on FPGAs"**  
*Paul Chow, University of Toronto*
- 12:30 - 14:15 Lunch
- 14:15 - 15:00 **Embedded Tutorial: "Enabling Software Engineers to Program Heterogeneous, Reconfigurable SoCs"**  
*Patrick Lysaght, Xilinx Research Labs*
- 15:00 - 15:30 **"Transparent Live Code Offloading on FPGA"**  
*Roberto Rigamonti, Baptiste Delporte, Anthony Convers, Alberto Dassatti*
- 15:30 - 16:00 Coffee Break
- 16:00 - 16:15 **"An interactive environment for mapping computational structures to FPGAs"**  
*Rinse Wester, Robert de Groote (Short Paper)*
- 16:15 - 16:45 **"Automated Inference of SoC Configuration through Firmware Source Code Analysis"**  
*Kris Heid, Ramon Wirsch, Christian Hochberger*
- 16:45 - 17:15 **"Building hardware from C# models"**  
*Kenneth Skovhede, Brian Vinter*
- 17:15 - 17:20 Workshop Closing

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QuickPlay: Software-Defined FPGA Platforms for Hardware-Augmented Applications